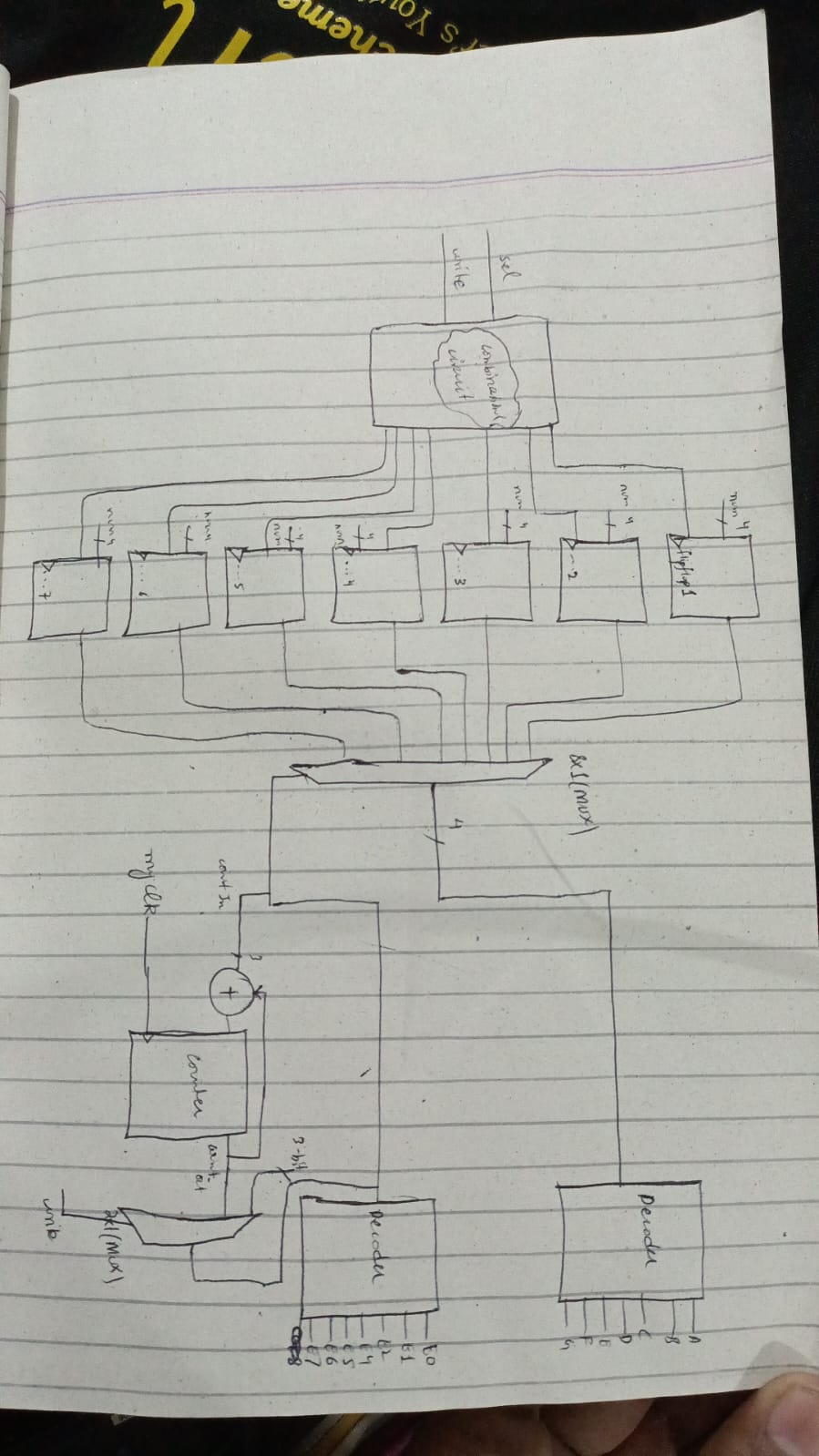
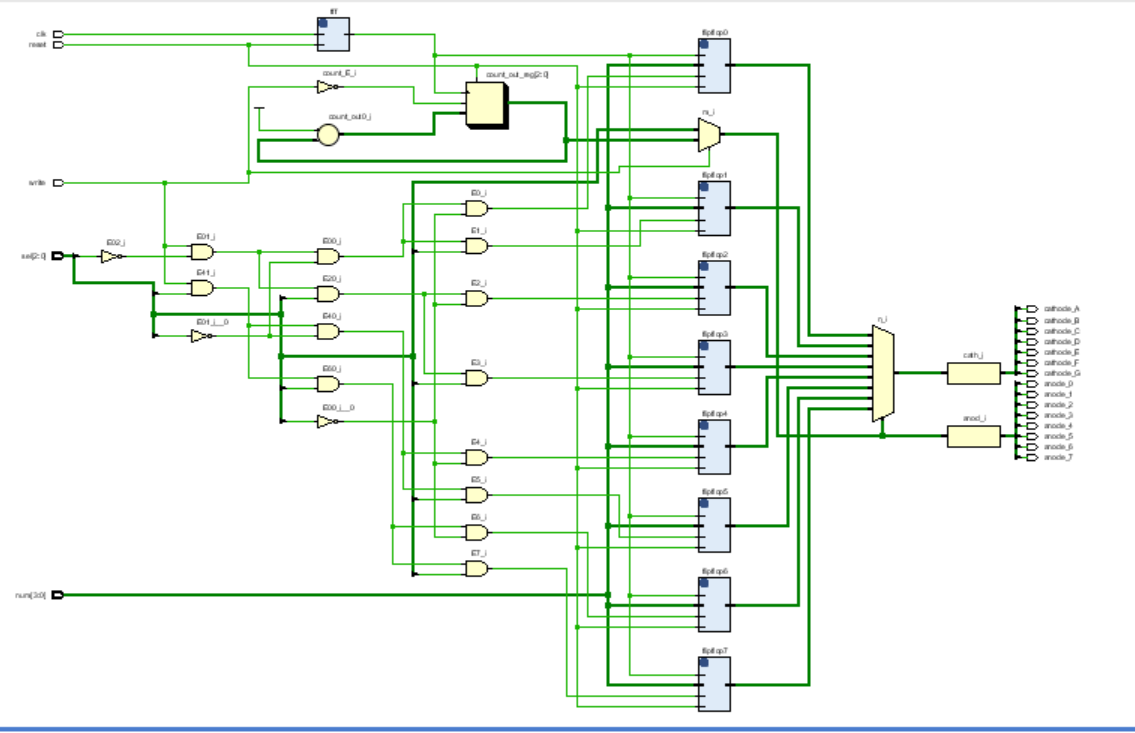
Name: NAQI-UL-HASSAN LAB7 REPORT Roll # no: 2022-EE-164

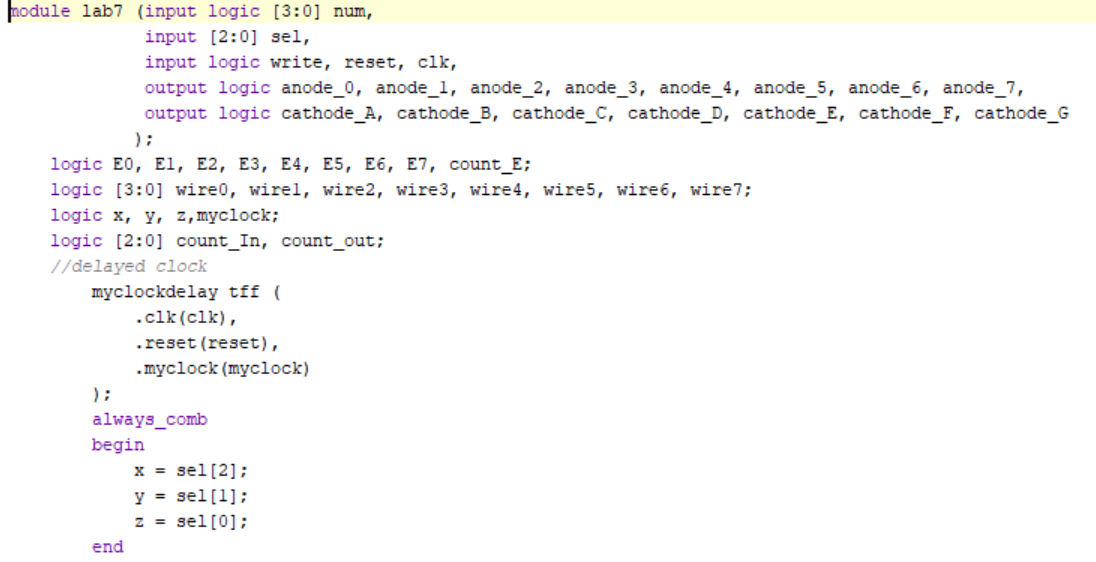
Circuit diagram (Handmade):

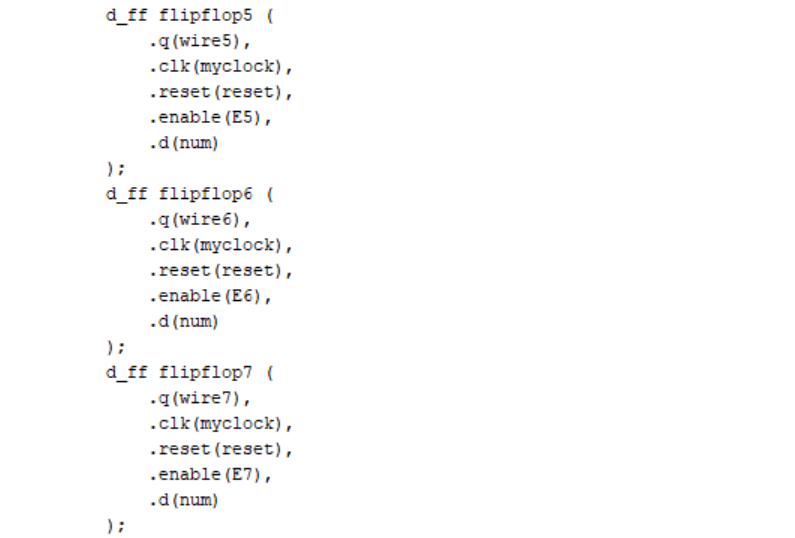
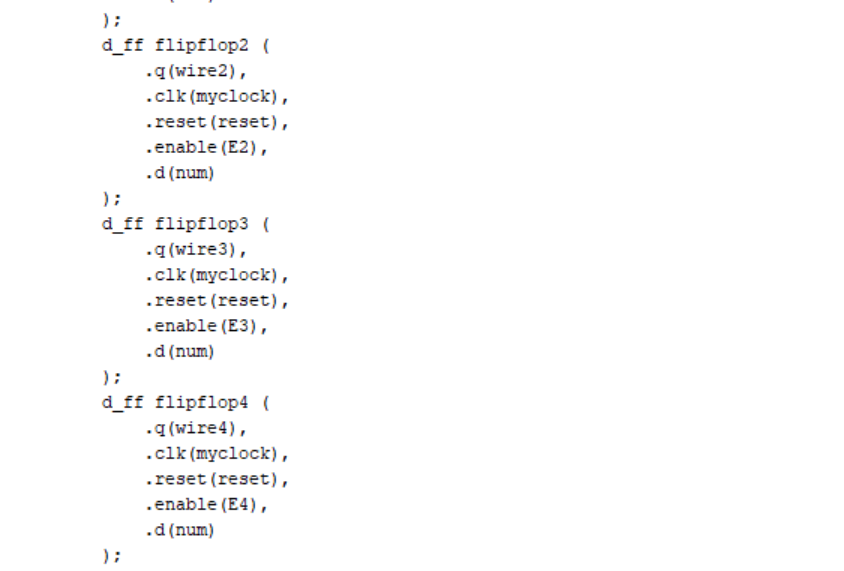
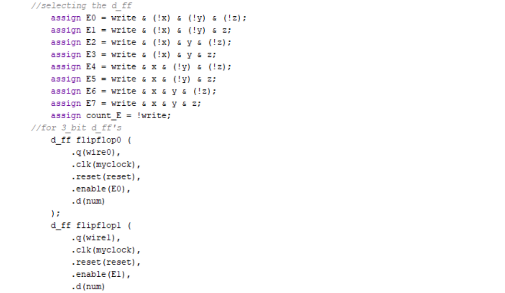


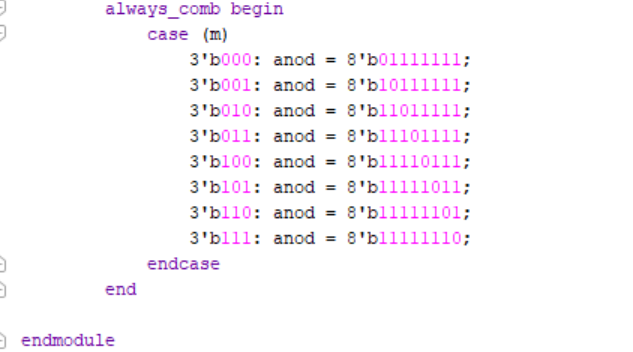
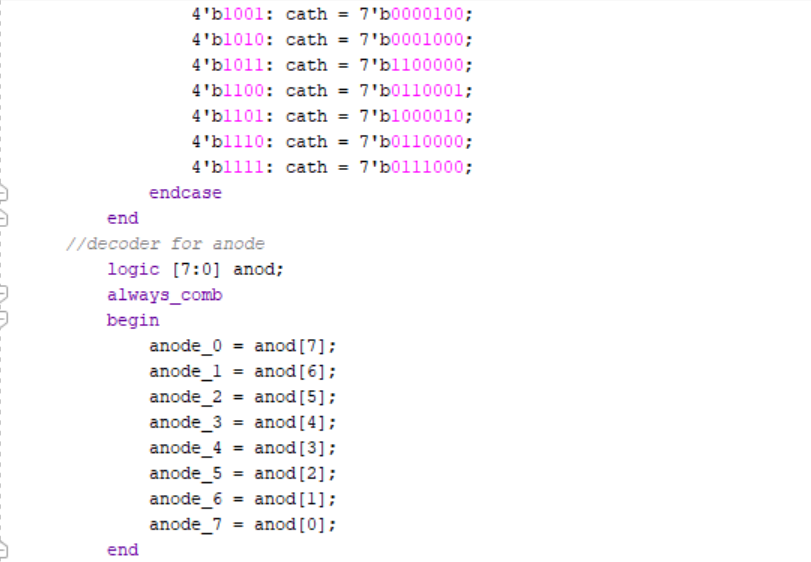
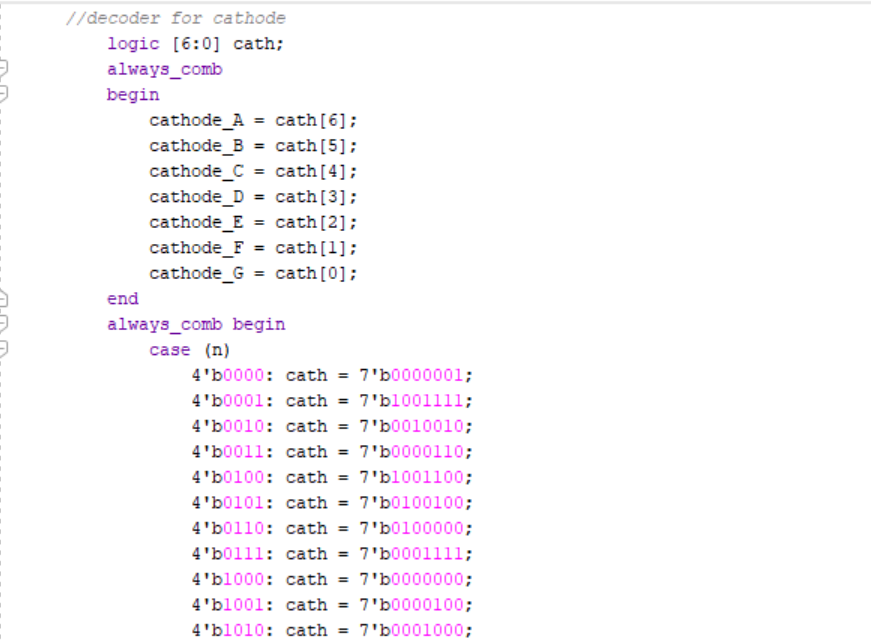
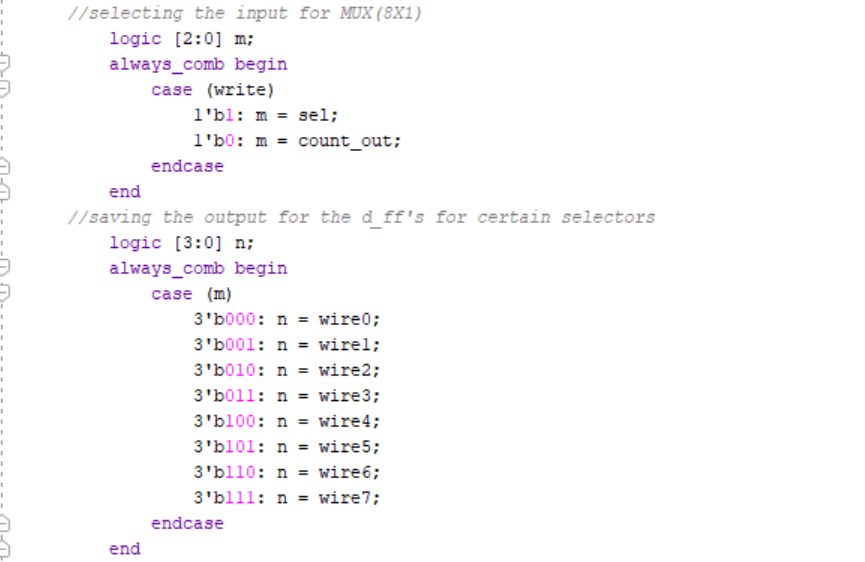
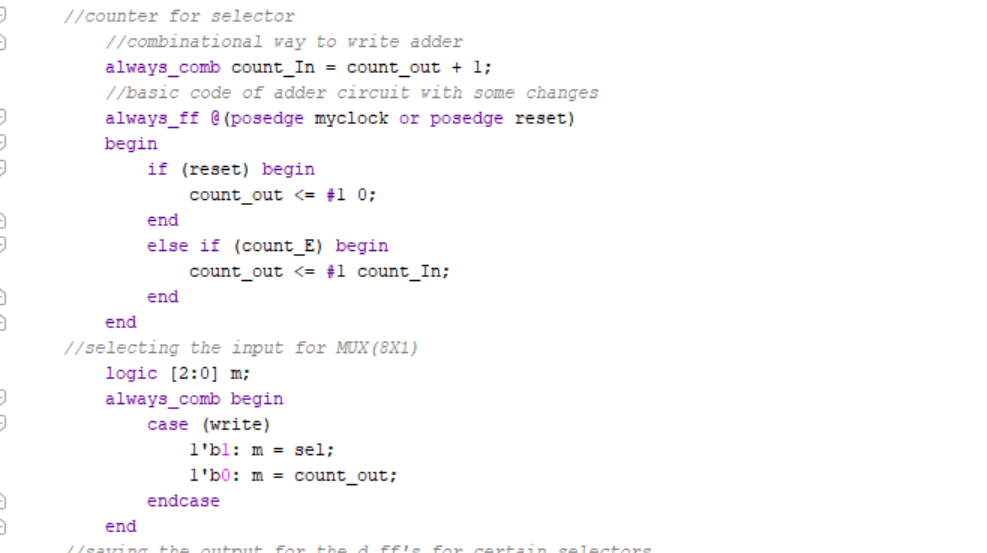
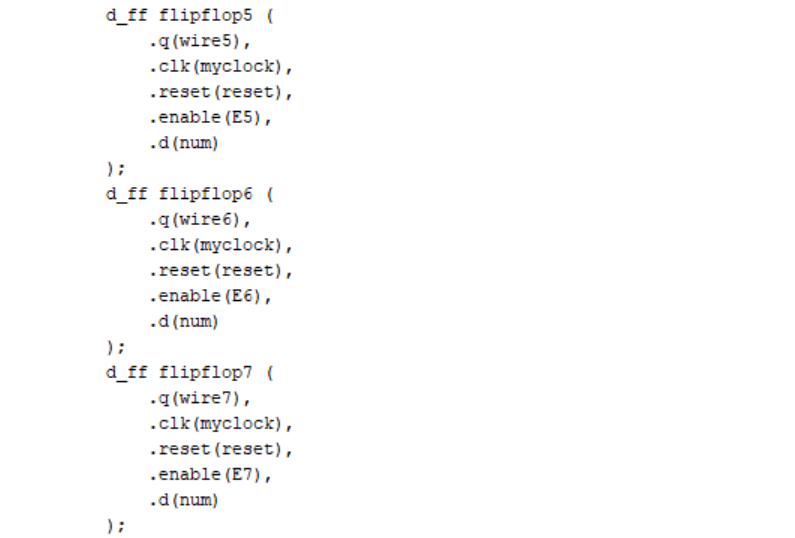
Circuit diagram (Elaborated):



Source Code:

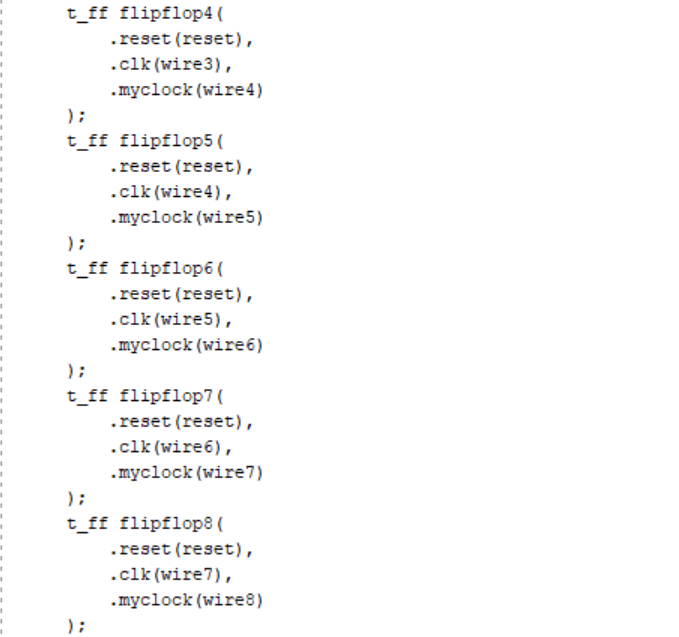


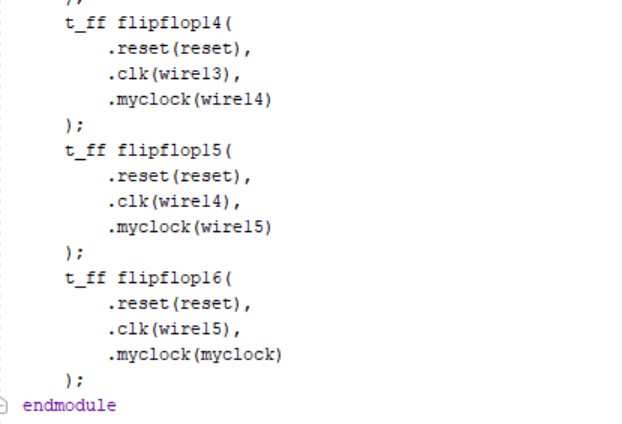


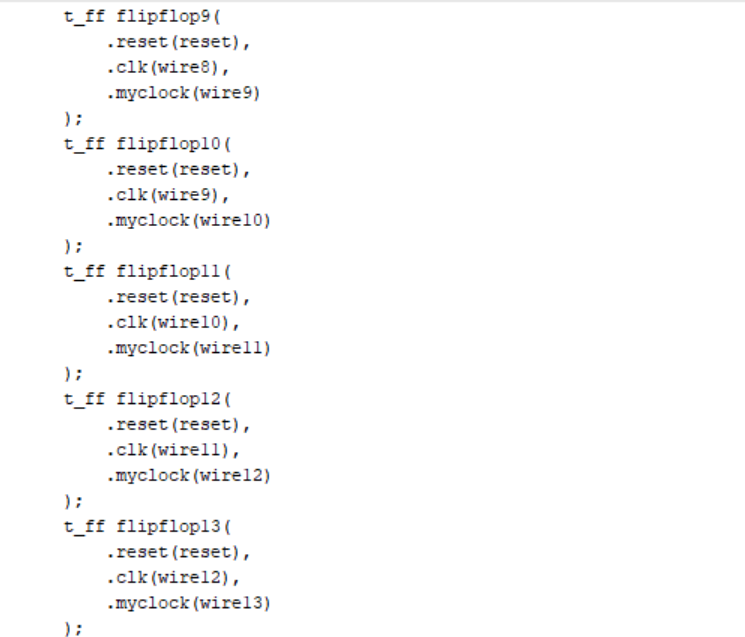


My clock:

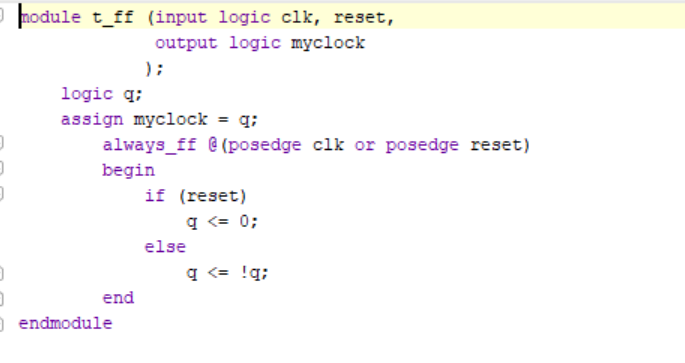




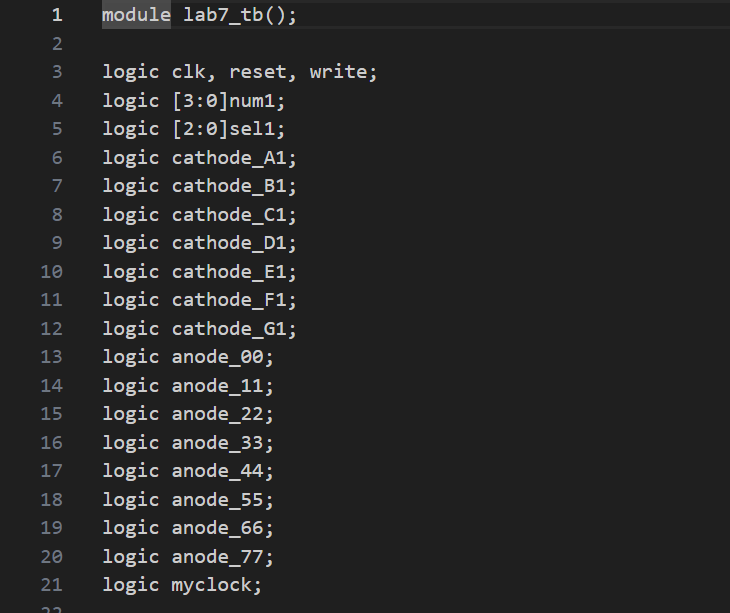


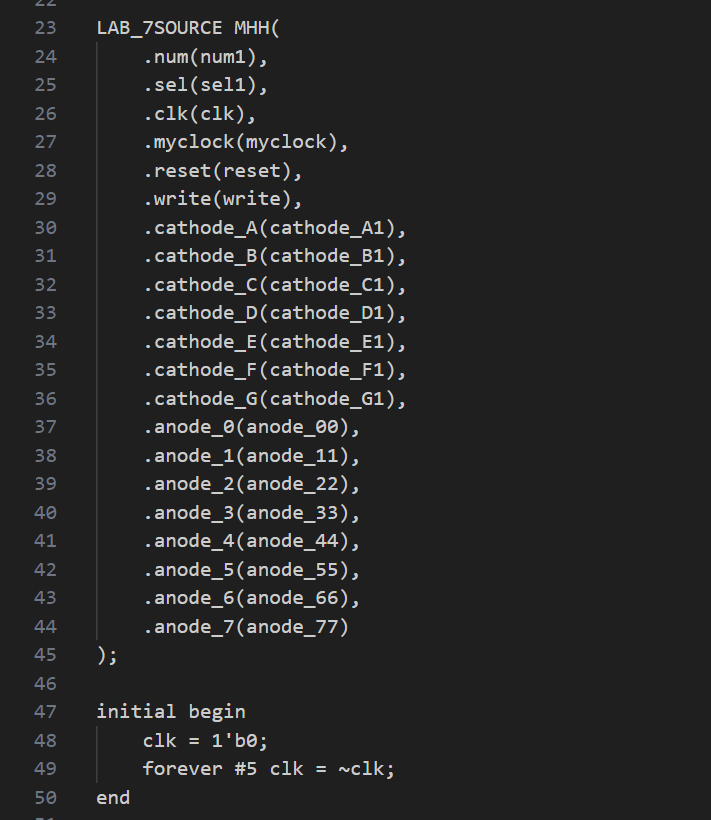


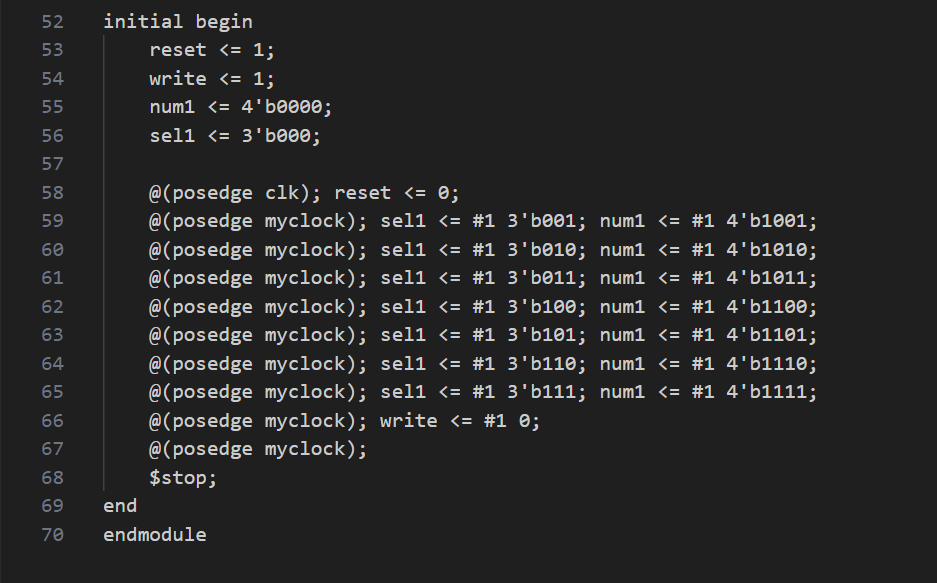
My toddle flipflop:



Testbench code:

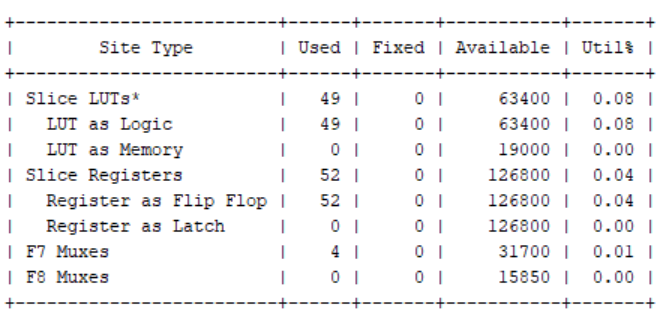


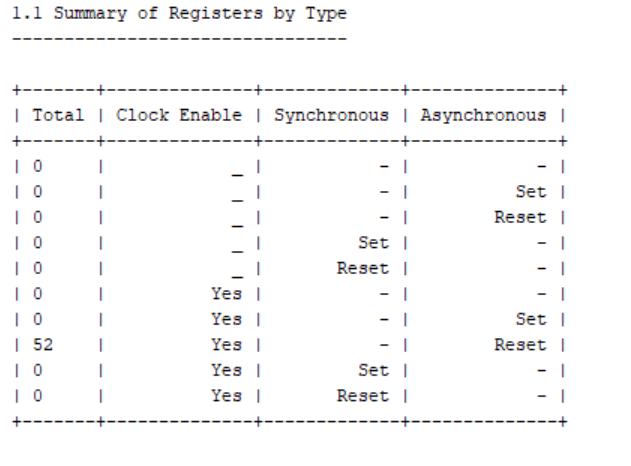


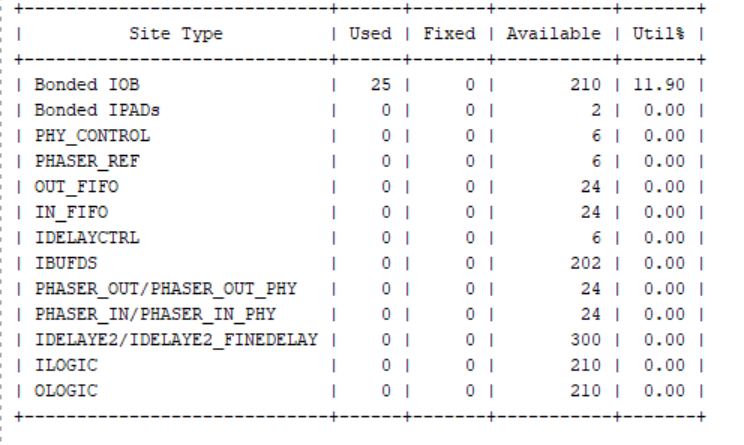
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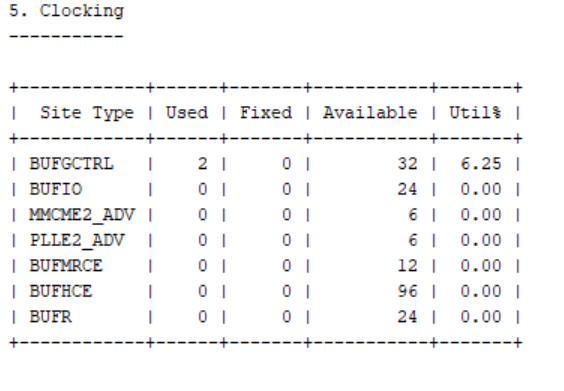
Resource utilization:

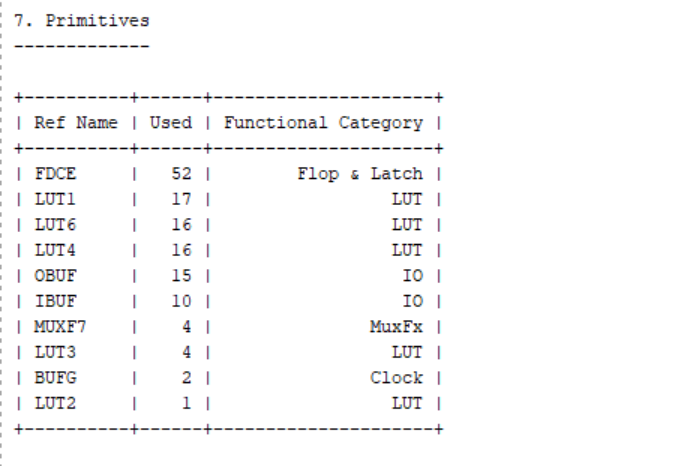
Following are the synthesis resource utilization:

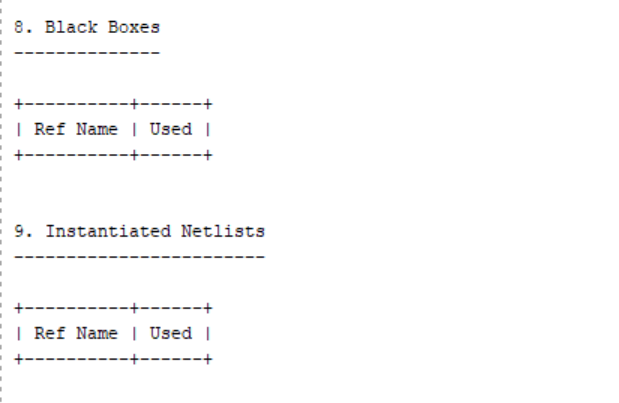




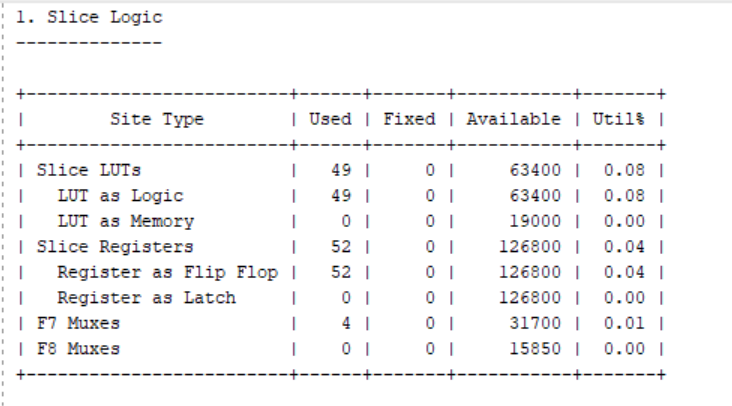


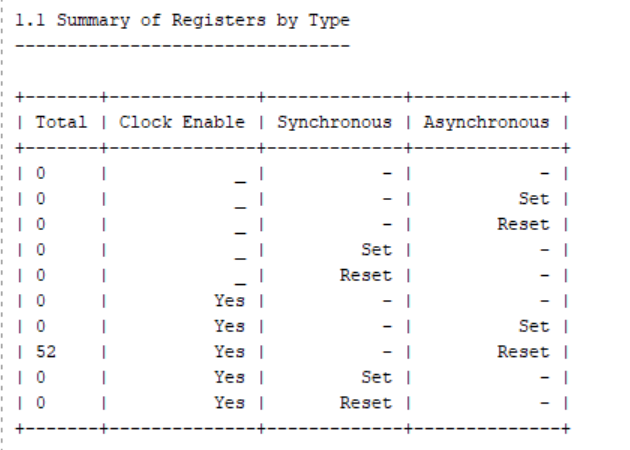


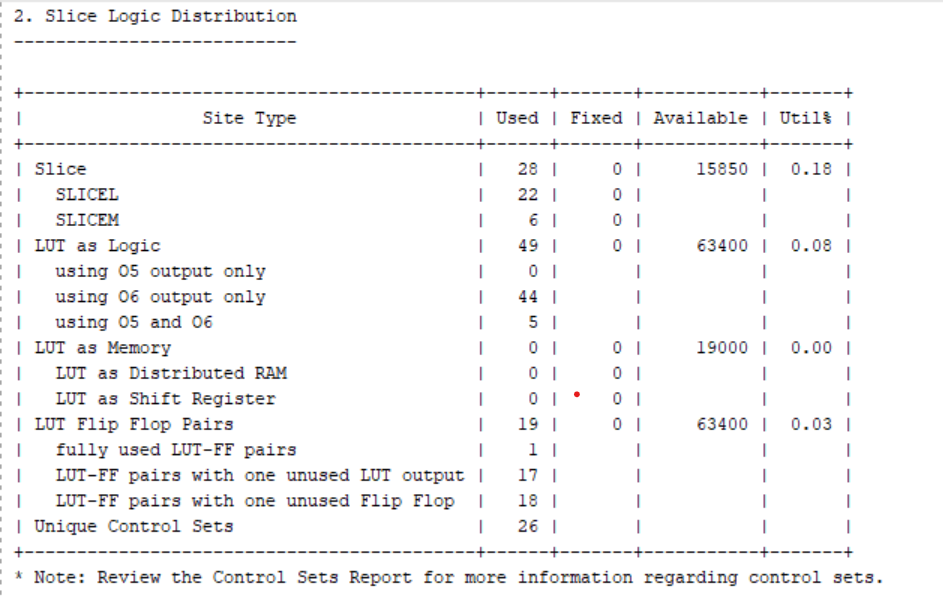


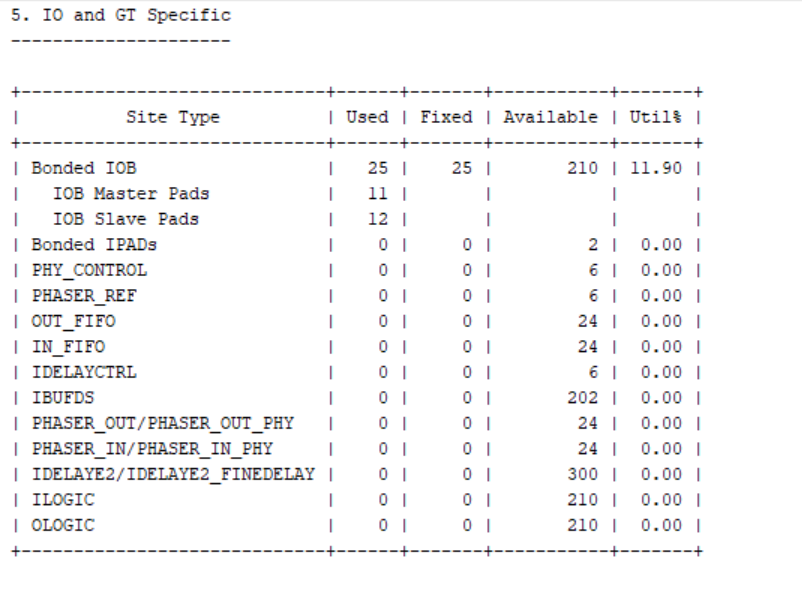


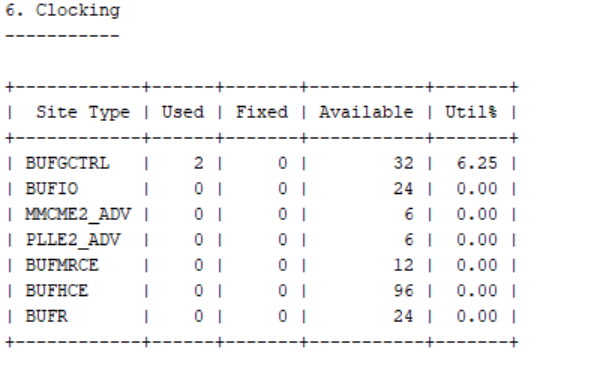
Following are the implementation resources:

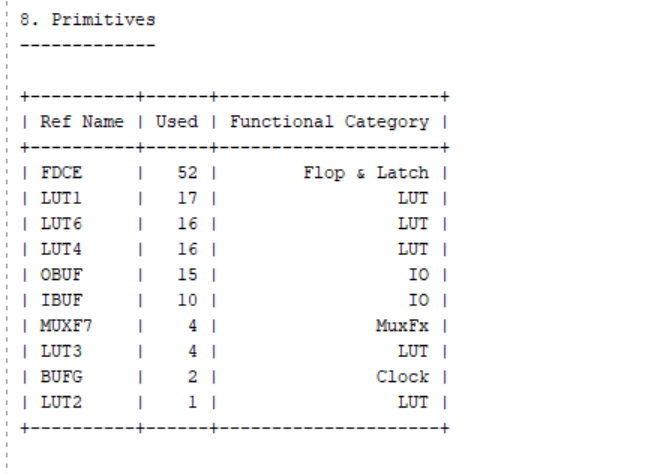


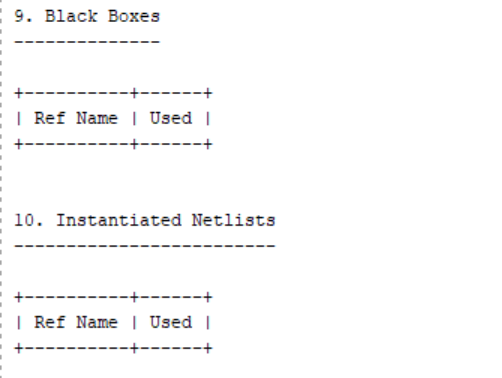




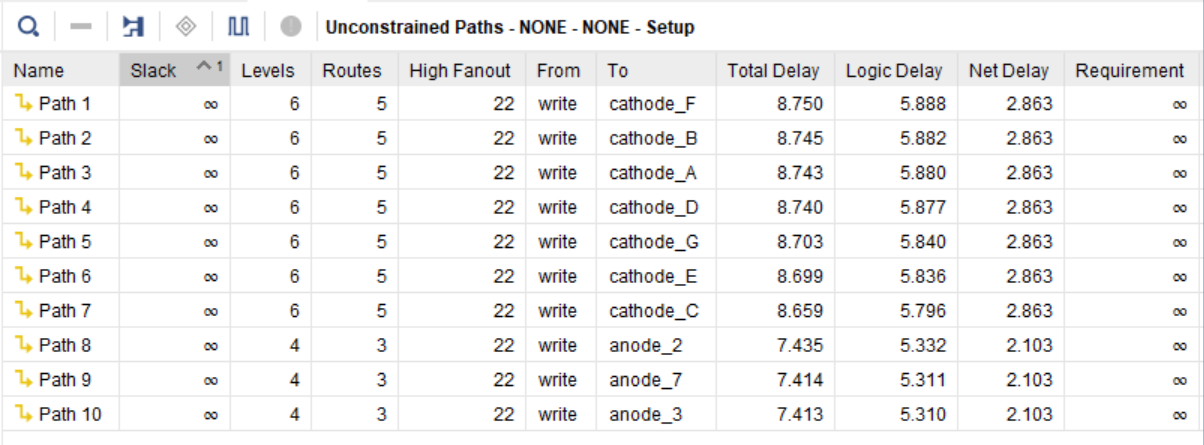


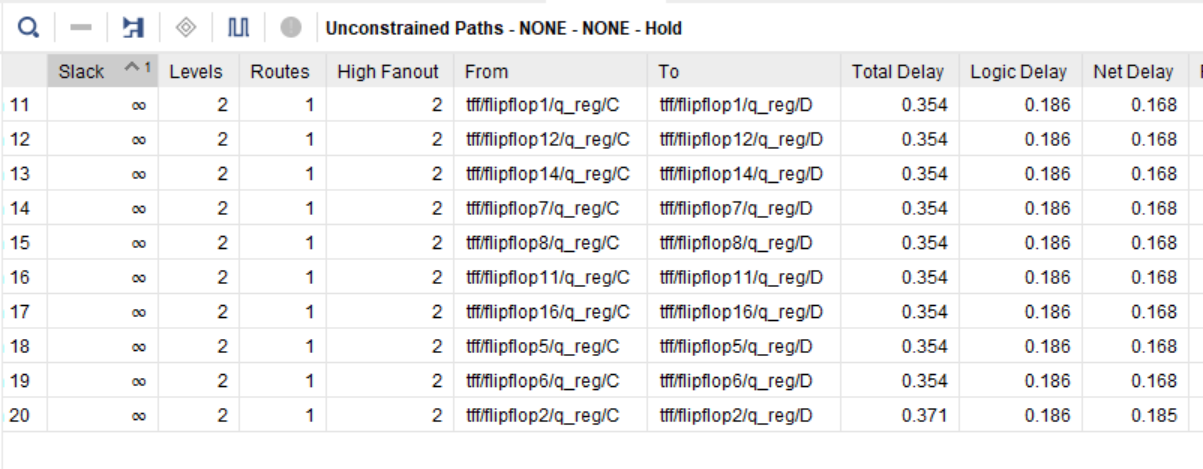
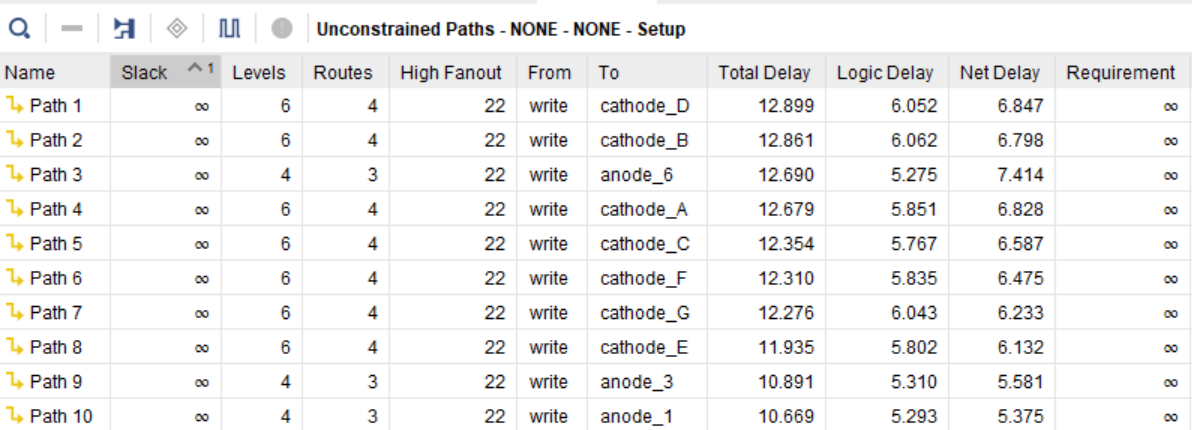
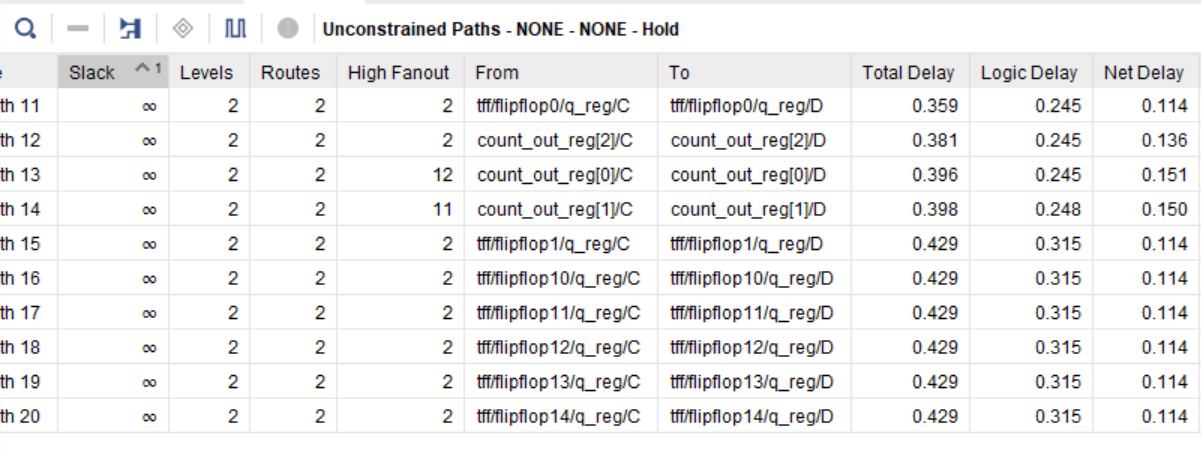




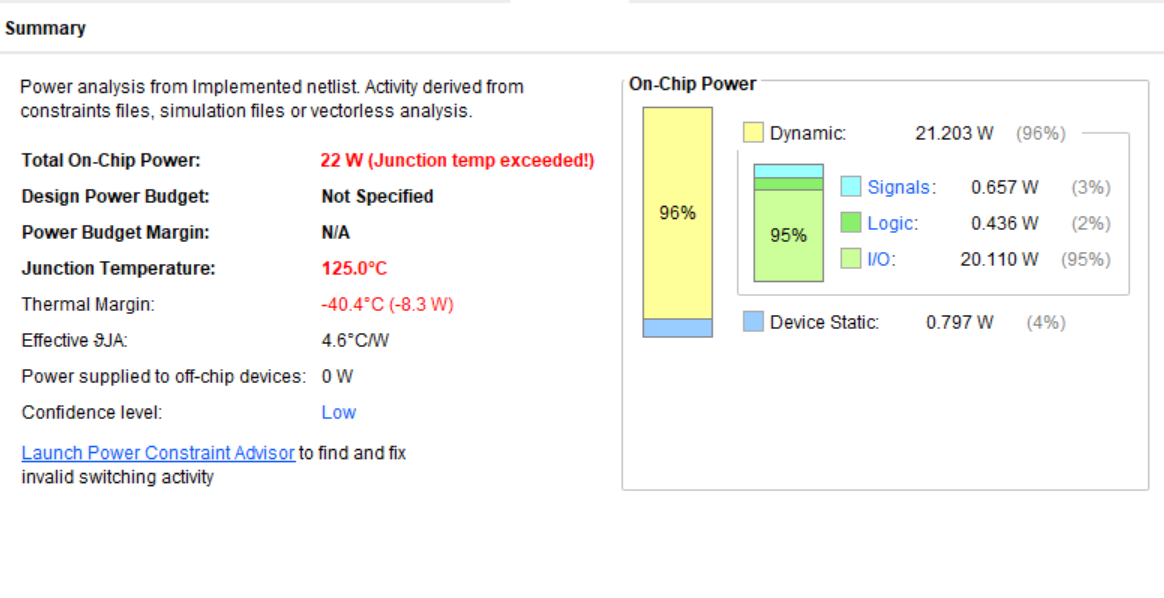


Timing Report:





Power Summary:



Simulation results:

